REMARKS

The present Amendment amends claims 1-3 and 7-15, leaves claims 4-6 and 16 unchanged and cancels claims 17-22. Therefore, the present application has pending claims 1-16.

Claims 1-16 stand rejected under 35 USC §102(e) as being anticipated by Hara (U.S. Patent No. 5,047,849). This rejection is traversed for the following reasons. Applicants submit that the features of the present invention as now recited in claims 1-16 are not taught or suggested by Hara whether taken individually or in combination with any of the other references of record. Therefore, Applicants respectfully request the Examiner to reconsider and withdraw this rejection.

Amendments were made to claims 1-16 so as to more clearly describe features of the present invention. Particularly, amendments were made to the claims to more clearly recite, for example, as illustrated in Figs. 1-3, 4a and 4b that the present invention is directed to an image display system, a television receiver including features of the image display system, an information processing device including features of the image display system, a transmitter including features of the image display system, and an image information transmission method including steps corresponding to features of the image display system.

The image display system according to the present invention as illustrated, for example, in Fig. 1 includes an image display unit 70 and a control unit 60 for outputting image information to the image display unit 70.

According to the present invention, the control unit 60 includes a block discrimination circuit portion 61e for discriminating a state of the image information

amounting to one frame among the image information in a pixel block unit, for example, 500 as illustrated in Fig. 2. Also, included in the image control unit 60 is an image processing portion 61c for processing the pixel block unit including the image information based on a discrimination result of the block discrimination circuit portion 61e, a storage portion 62 for storing the image information processed by the image processing portion 61c and a synchronizing signal generation portion 61b for reading the image information from the storage portion 62, controlling a clock 61a in accordance with read image information and outputting the read image information to the image display unit 70.

Unique according to the present invention is that the image processing portion 61c processes the pixel block unit 500 such that either each of a plurality of pixel block areas included in the pixel block unit is rewritten for each of a plurality of frames as per, for example, elements 530 and 530a-e as illustrated in Figs. 2 and 4b, or each pixel block area remains the same for a plurality of frames of the frames such as per elements 510 and 510a-e as illustrated in Figs. 2 and 4a.

The above described features of the present invention now more clearly recited in the claims are not taught or suggested by Hara whether taken individually or in combination with any of the other references of record.

Hara discloses an image display apparatus in Figs. 9 and 10 thereof which detects the presence or absence of motion of each picture element at a present frame and a preceding frame of a video signal to be inputted so that when each picture element at one field by interlace scanning is driven, the picture element is driven based on the video signal at the one field and when non-motion is detected,

the same is driven based on the video signal at a field preceding the one field. Thus, Hara simply teaches that when a still or moving picture image is displayed, such display is accomplished by interlacing wherein one of odd or even lines of (fields) the image are used in alternative frames. This teaching of Hara does not anticipate nor render obvious the features of the present invention.

The present invention provides, for example, as illustrated in Figs. 2 and 4a and b that for a still picture for each 2 x 2 pixel block area, each pixel of the 2 x 2 pixel block area is displayed across four frames, whereas in a moving picture an average of each 2 x 2 pixel block area is written into each of the four pixels of the 2 x 2 pixel block area for each frame, thereby updating each succeeding. These features of the present invention are clearly not taught or suggested by Hara.

Therefore, Hara fails to teach or suggest a block discrimination circuit portion for discriminating a state of the image information amounting to one frame among the image information in a pixel block unit and an image processing portion for processing the pixel block unit including the image information based on a discriminated result of the block discrimination circuit portion as recited in the claims.

Further, Hara fails to teach or suggest that the image processing portion processes the pixel block unit such that either each of a plurality of pixel block areas included in the pixel block unit is rewritten for each of a plurality of frames, or each pixel block area remains the same for a plurality of the frames as recited in the claims.

Thus, as is clear from the above, Hara fails to teach or suggest the features of the present invention as now more clearly recited in the claims. Therefore,

reconsideration and withdrawal of the 35 USC §102(e) rejection of claims 1-16 as being anticipated by Hara is respectfully requested.

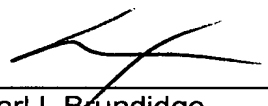
The remaining references of record have been studied. Applicants submit that they do not supply any of the deficiencies noted above with respect to the reference utilized in the rejection of claims 1-16.

In view of the foregoing amendments and remarks, Applicants submit that claims 1-16 are in condition for allowance. Accordingly, early allowance of claims 1-16 is respectfully requested.

To the extent necessary, the applicants petition for an extension of time under 37 CFR 1.136. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, or credit any overpayment of fees, to the deposit account of Antonelli, Terry, Stout & Kraus, LLP, Deposit Account No. 01-2135 (500.40686X00).

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP



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